

FIG. 1A

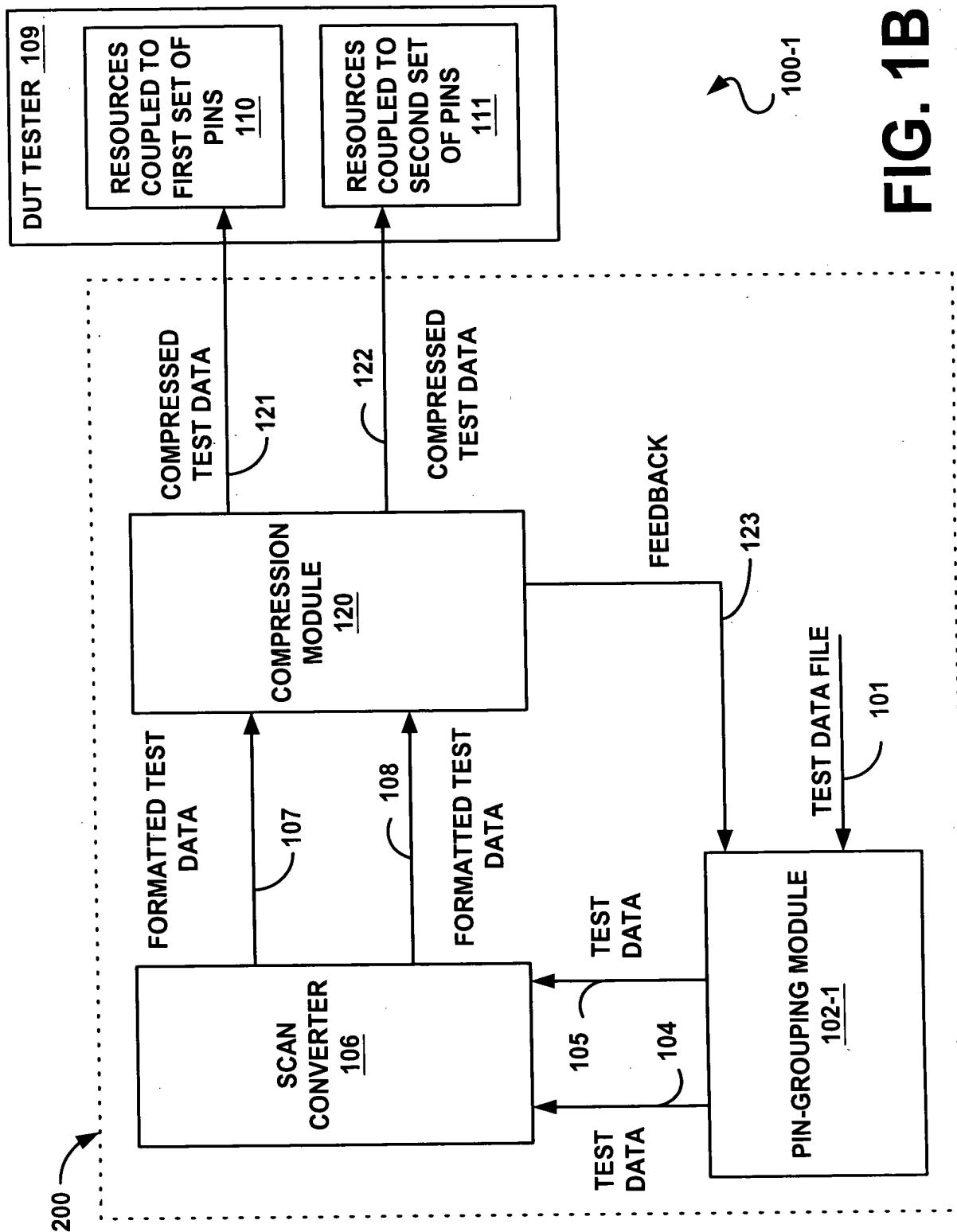


FIG. 1B

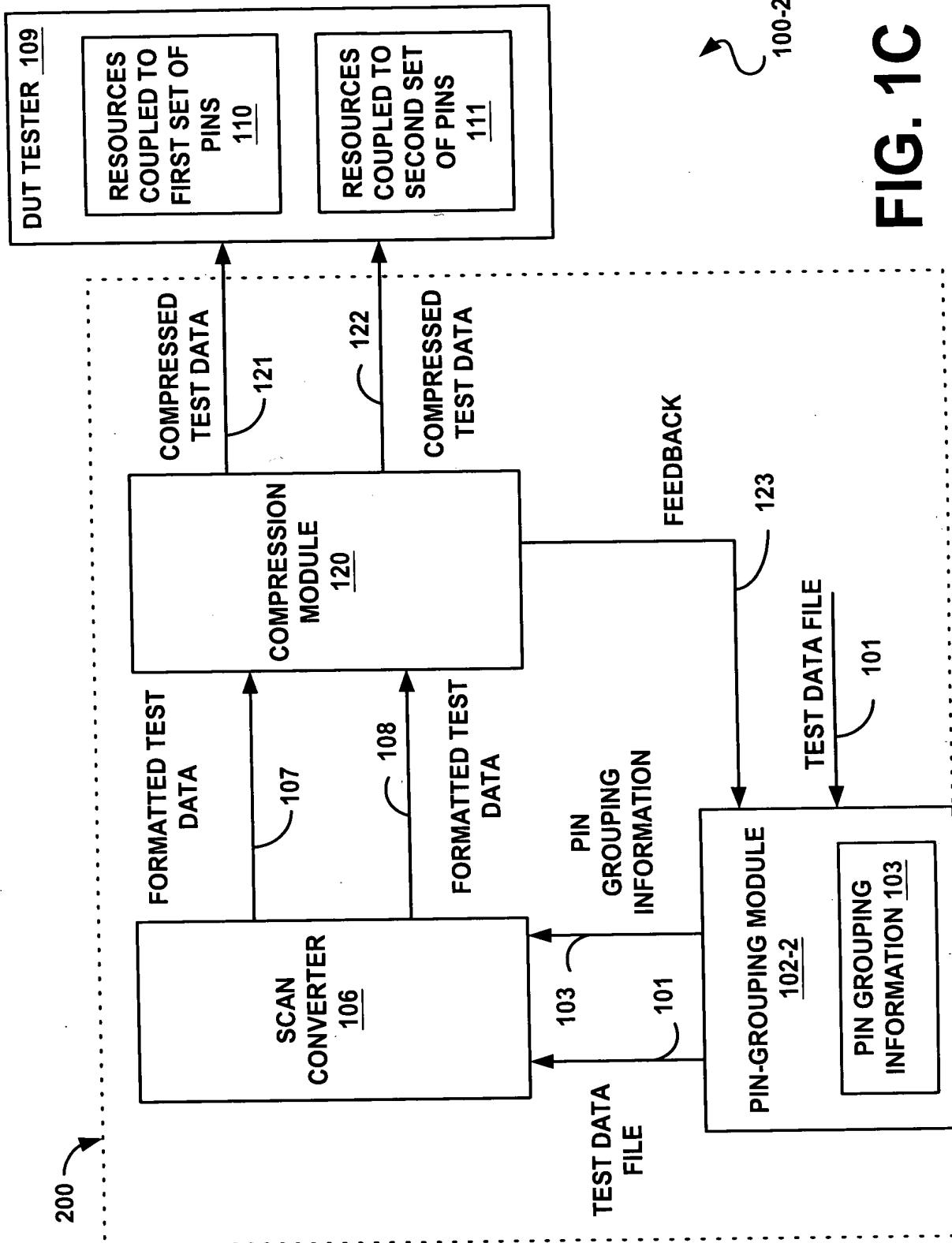


FIG. 1C

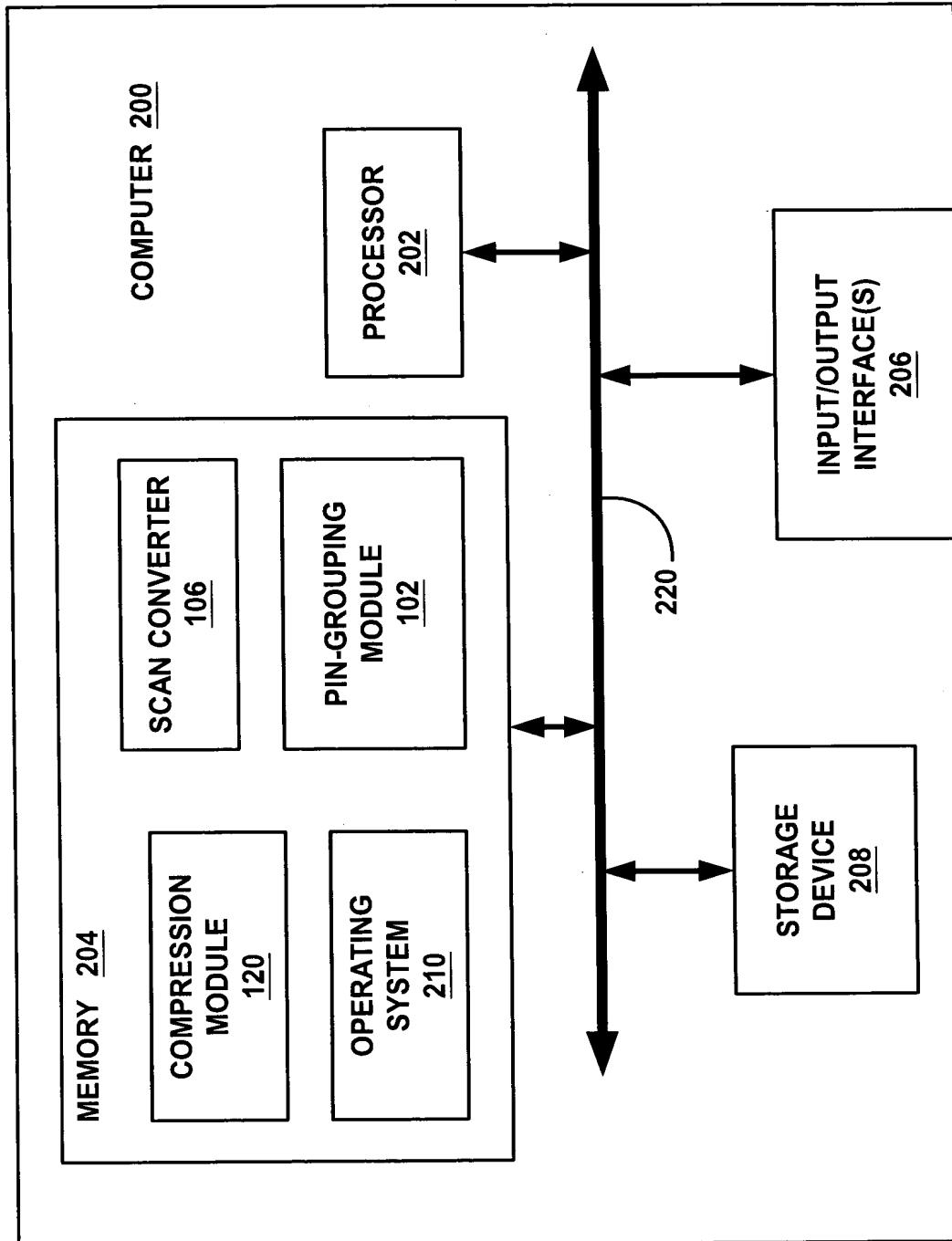


FIG. 2

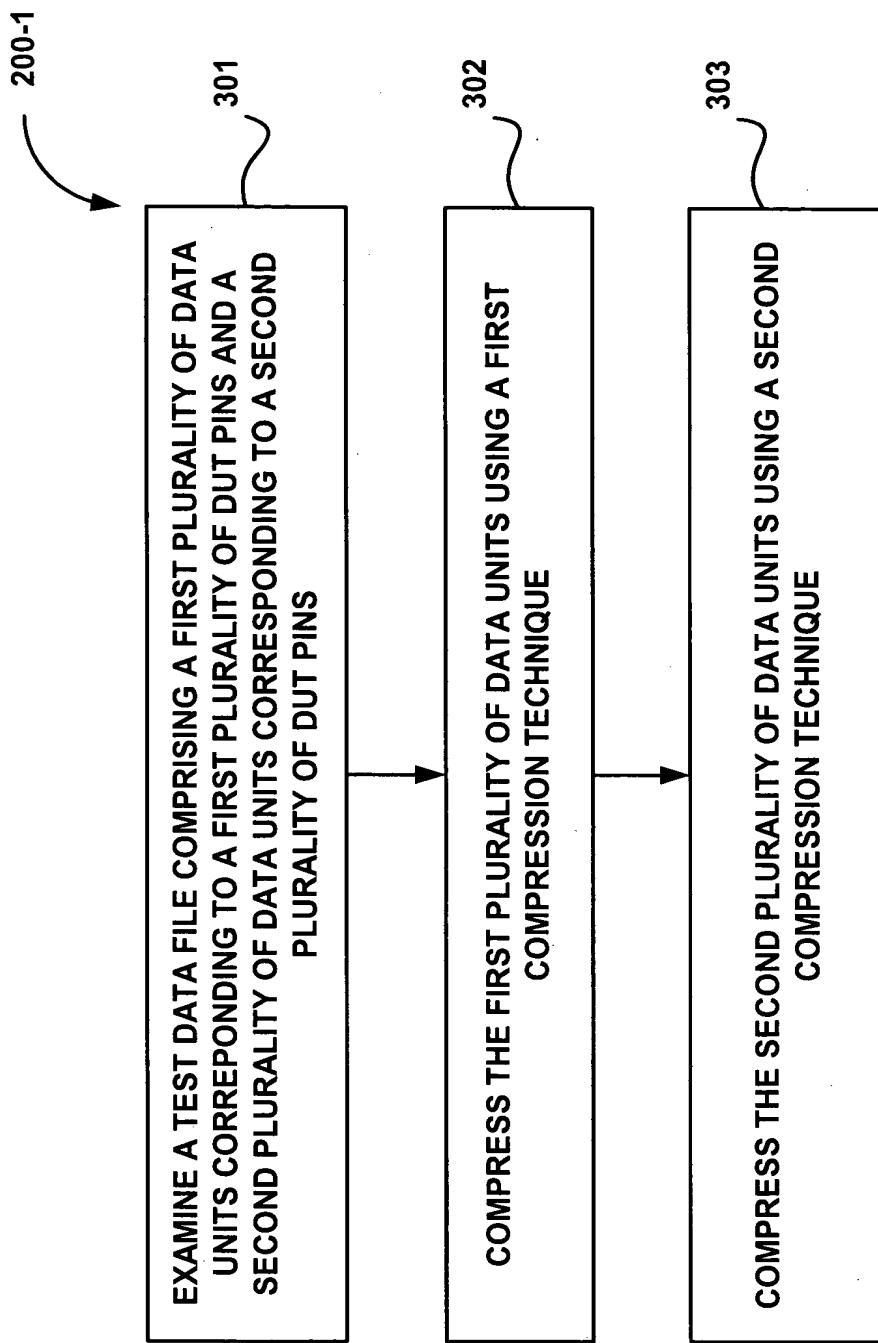


FIG. 3

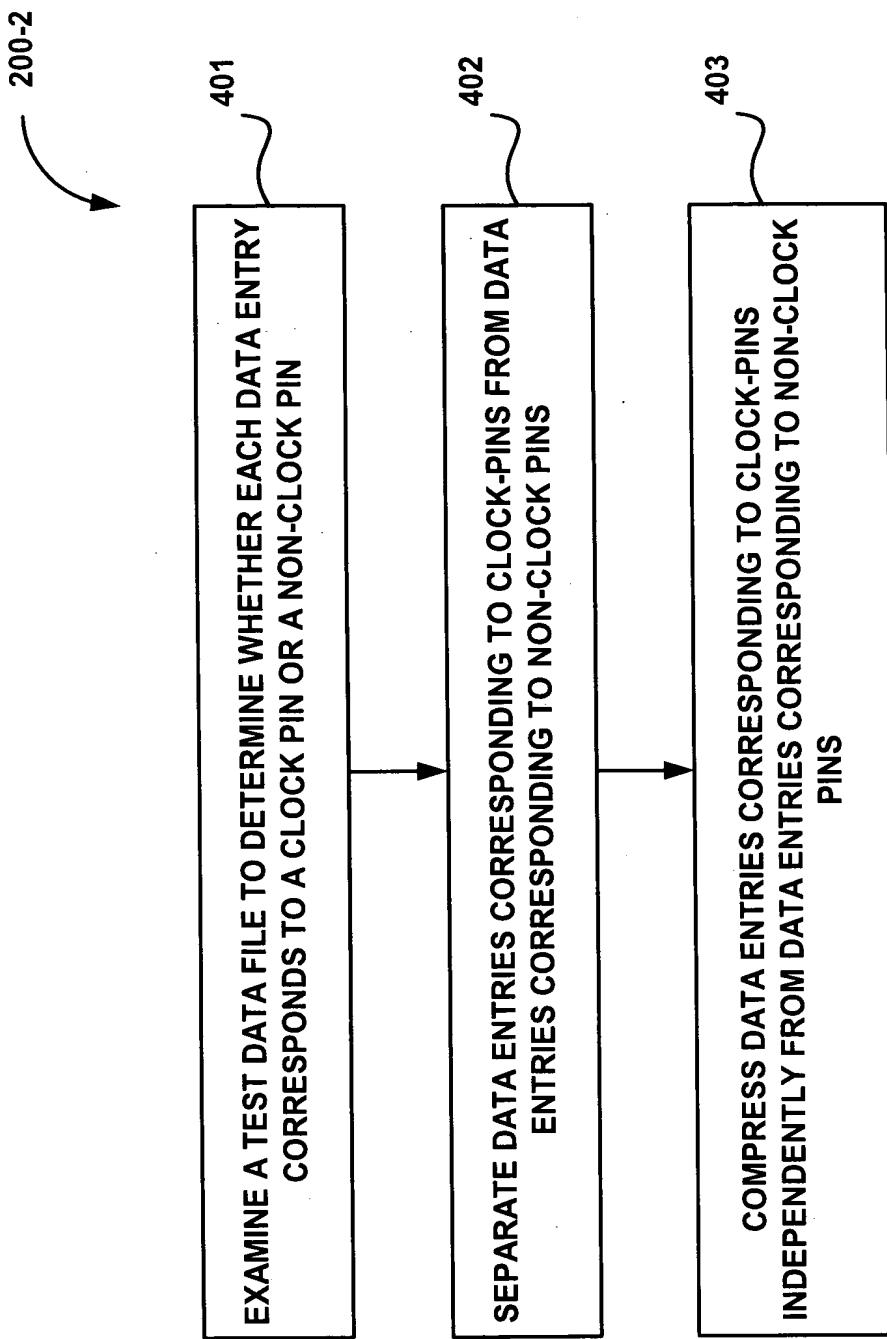


FIG. 4A

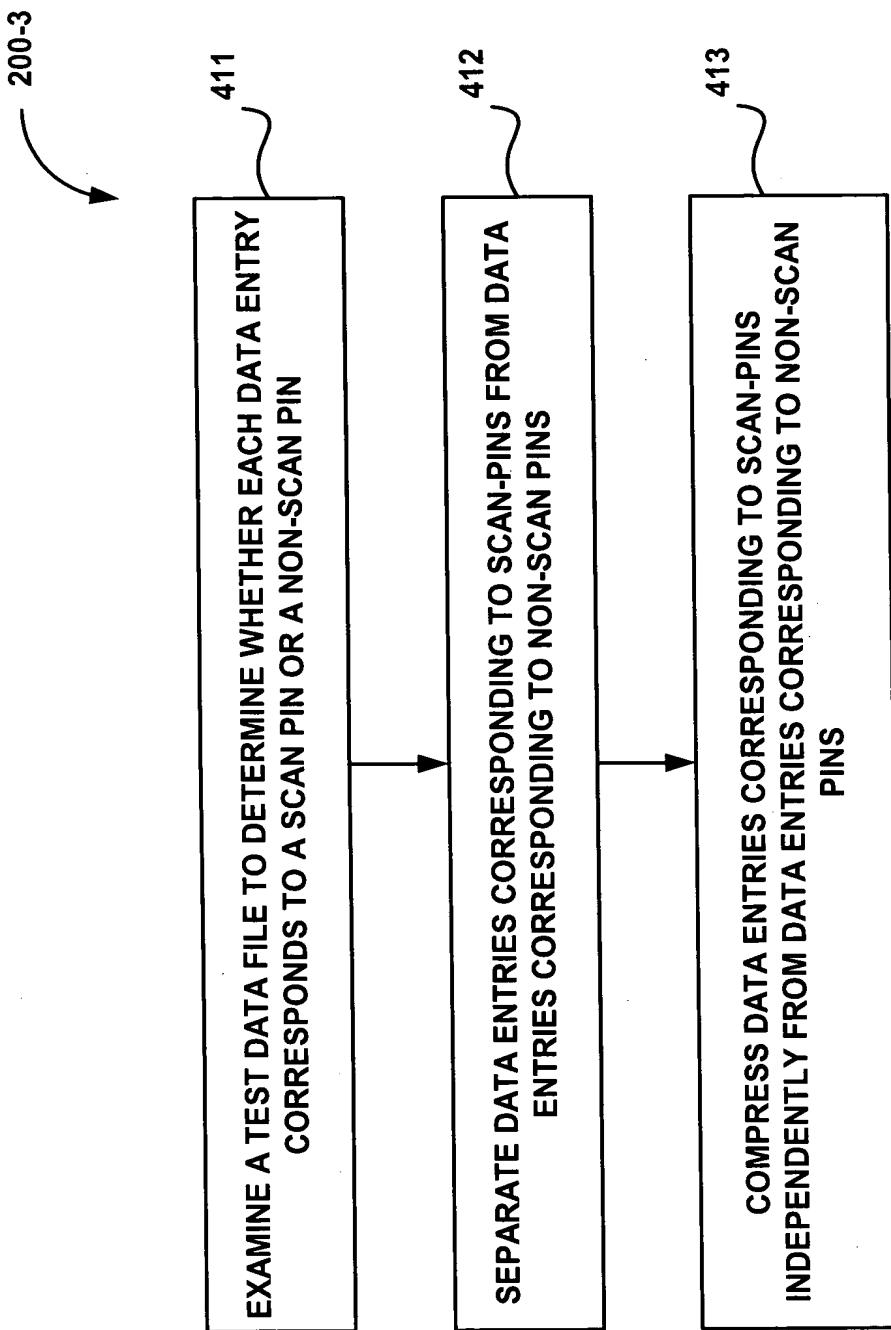


FIG. 4B

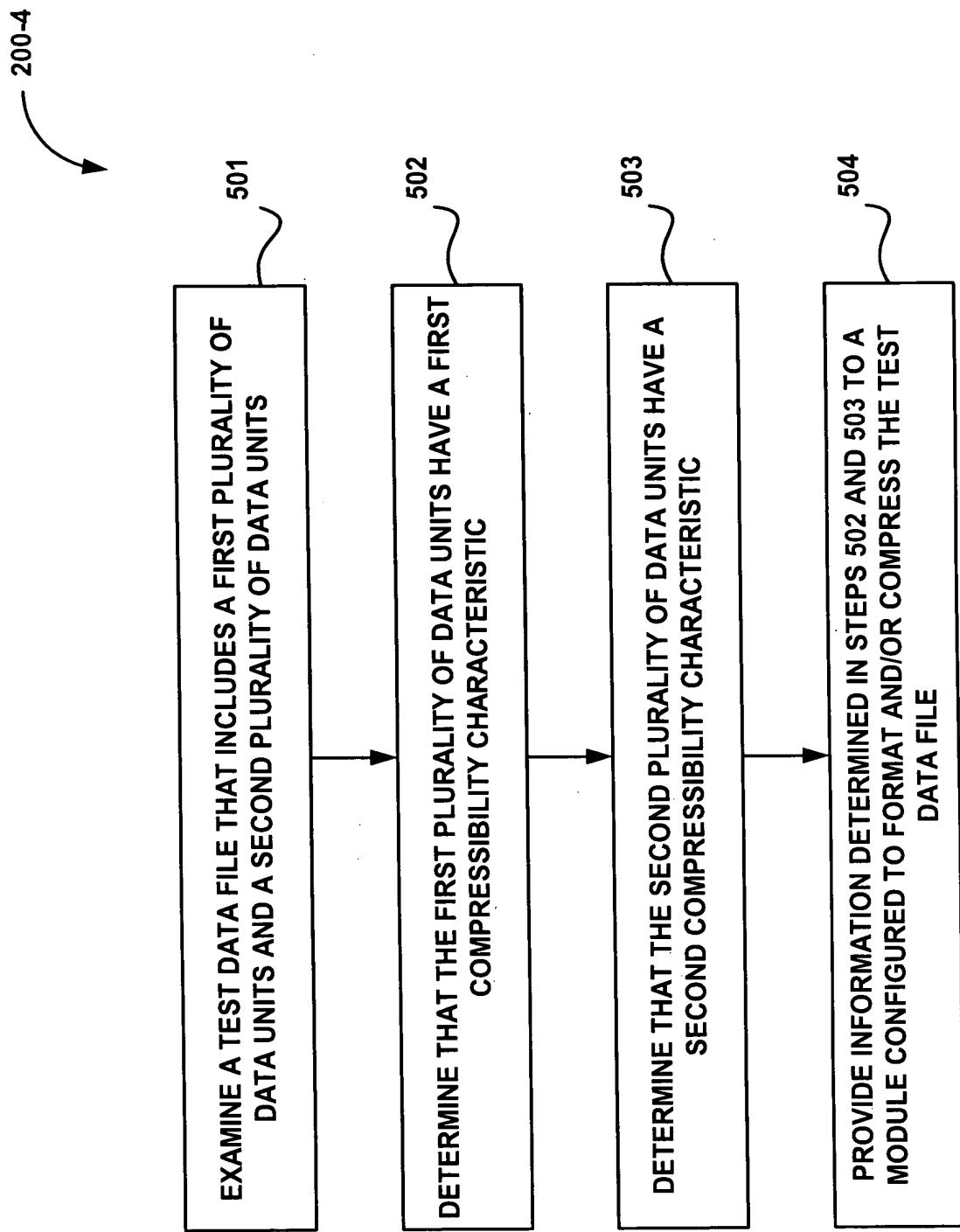


FIG. 5

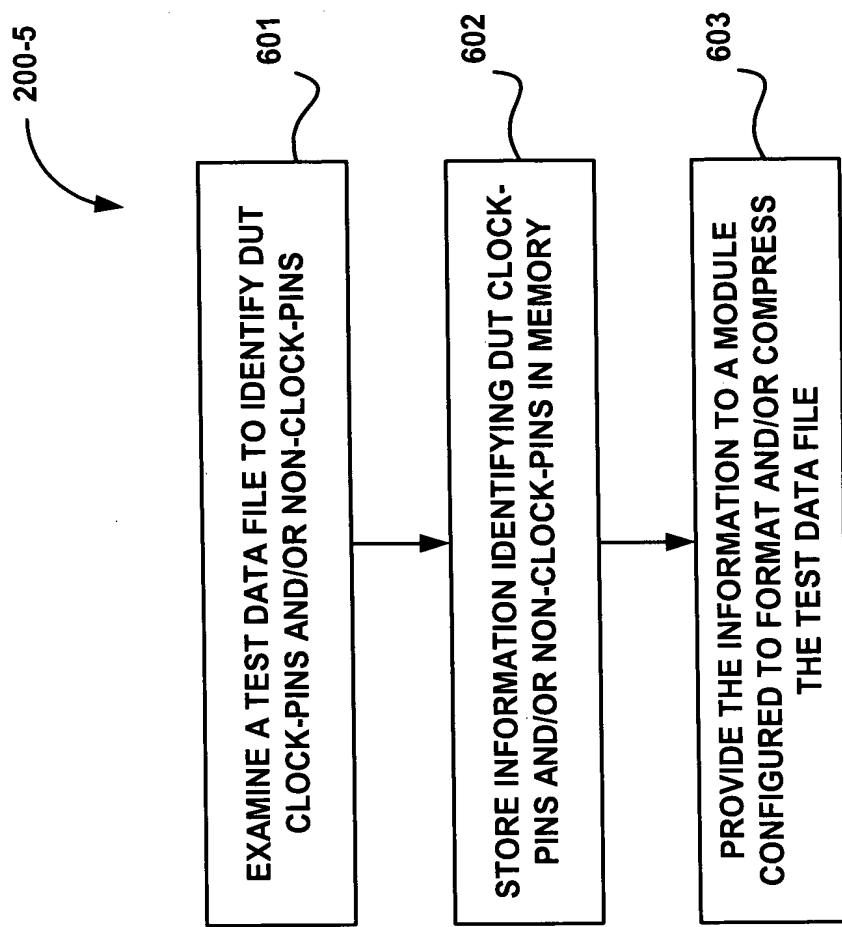


FIG. 6A

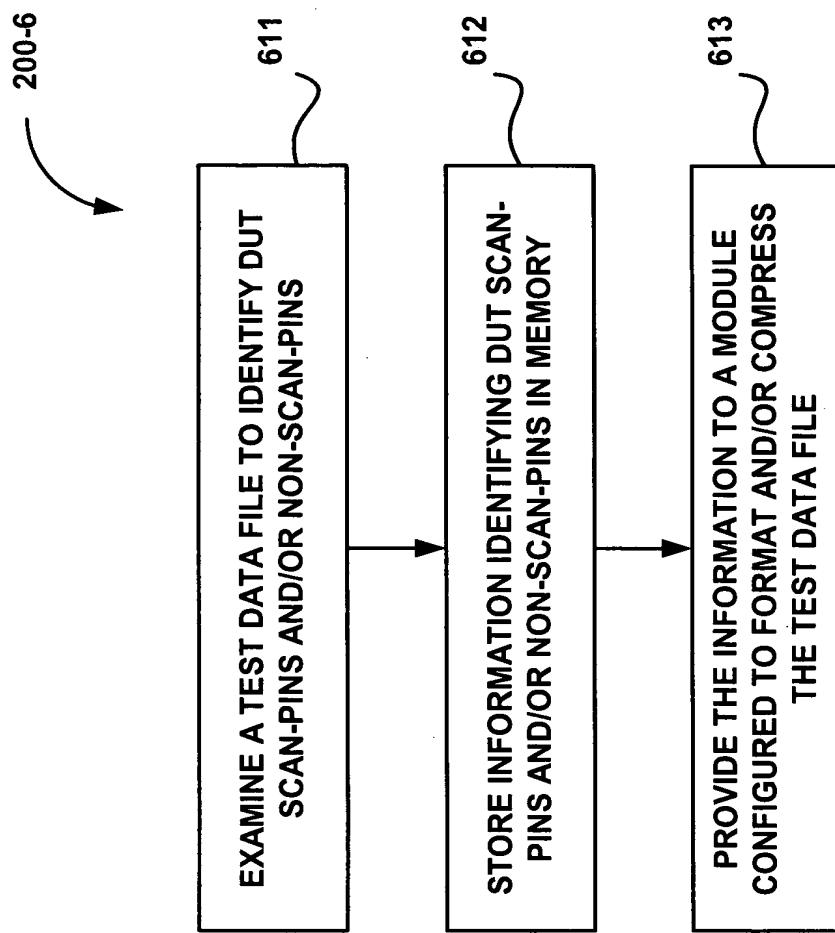


FIG. 6B

100-3

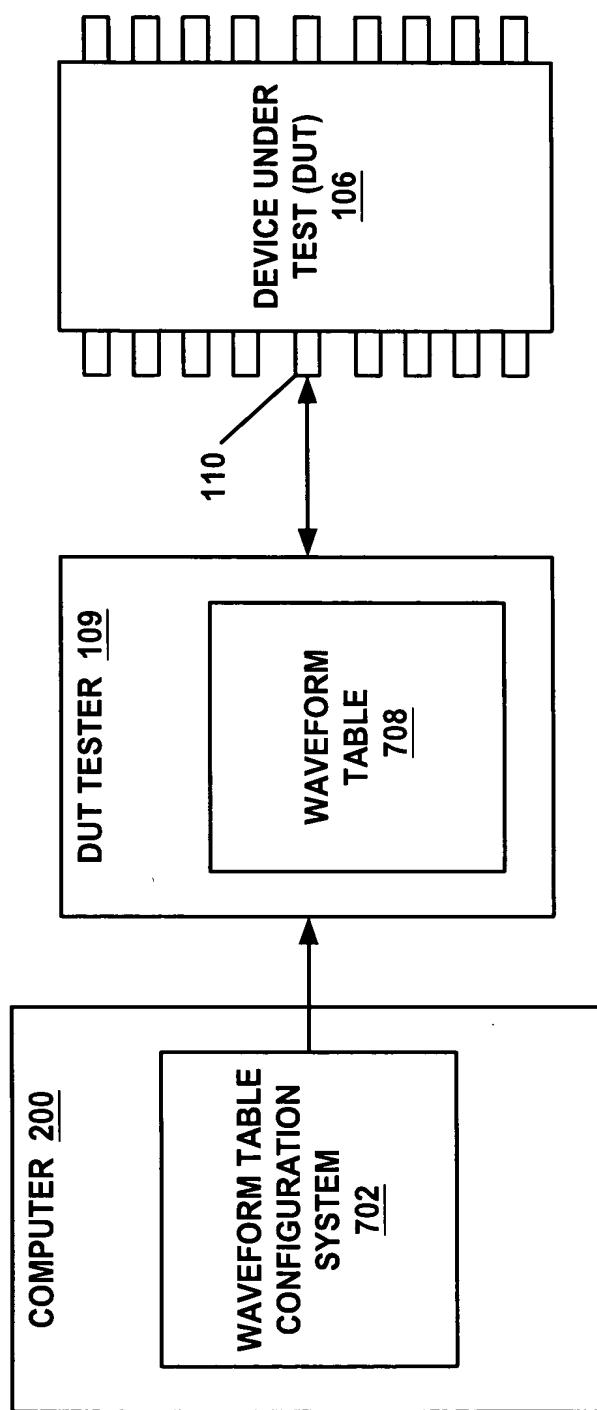


FIG. 7

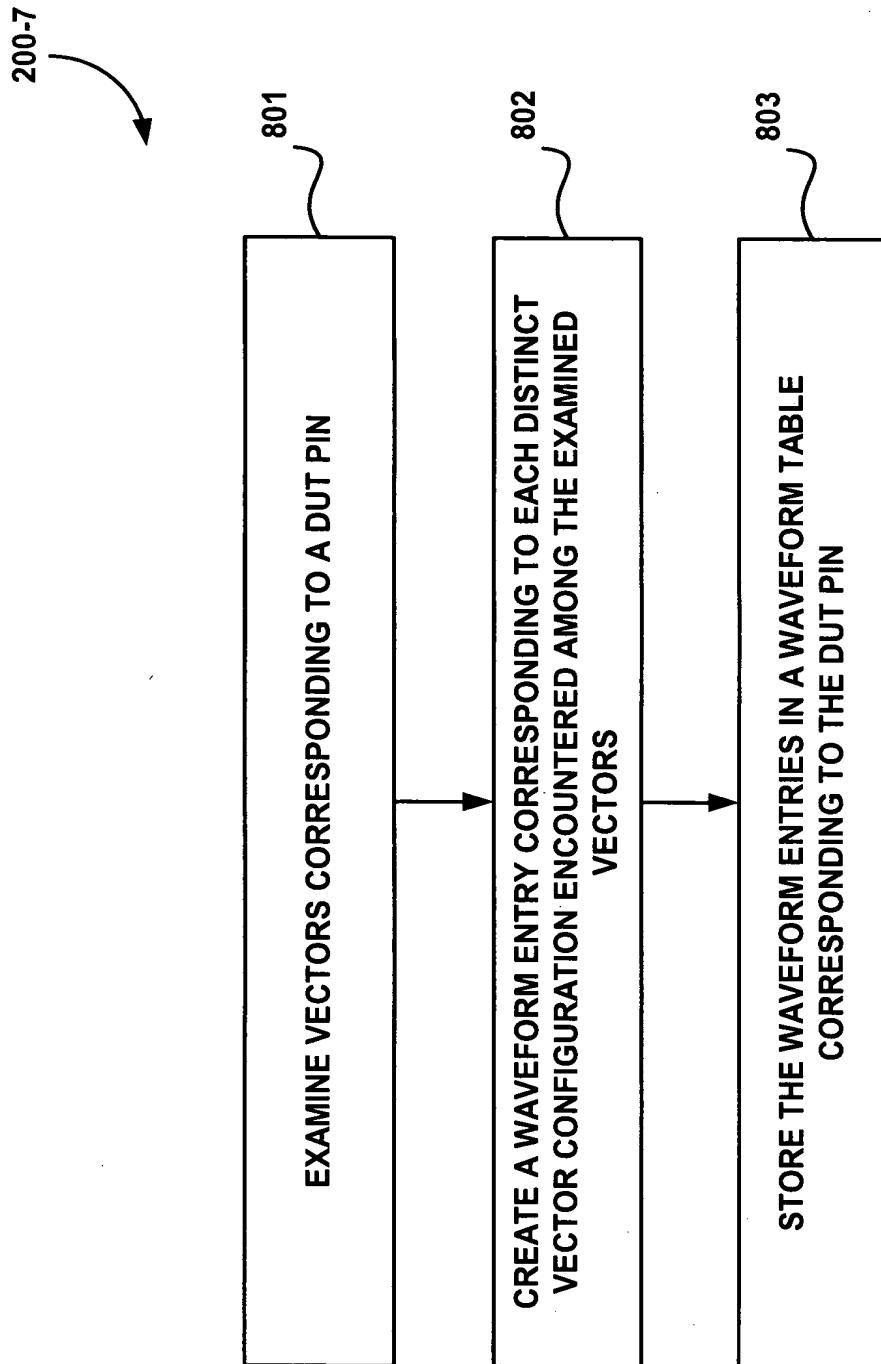


FIG. 8

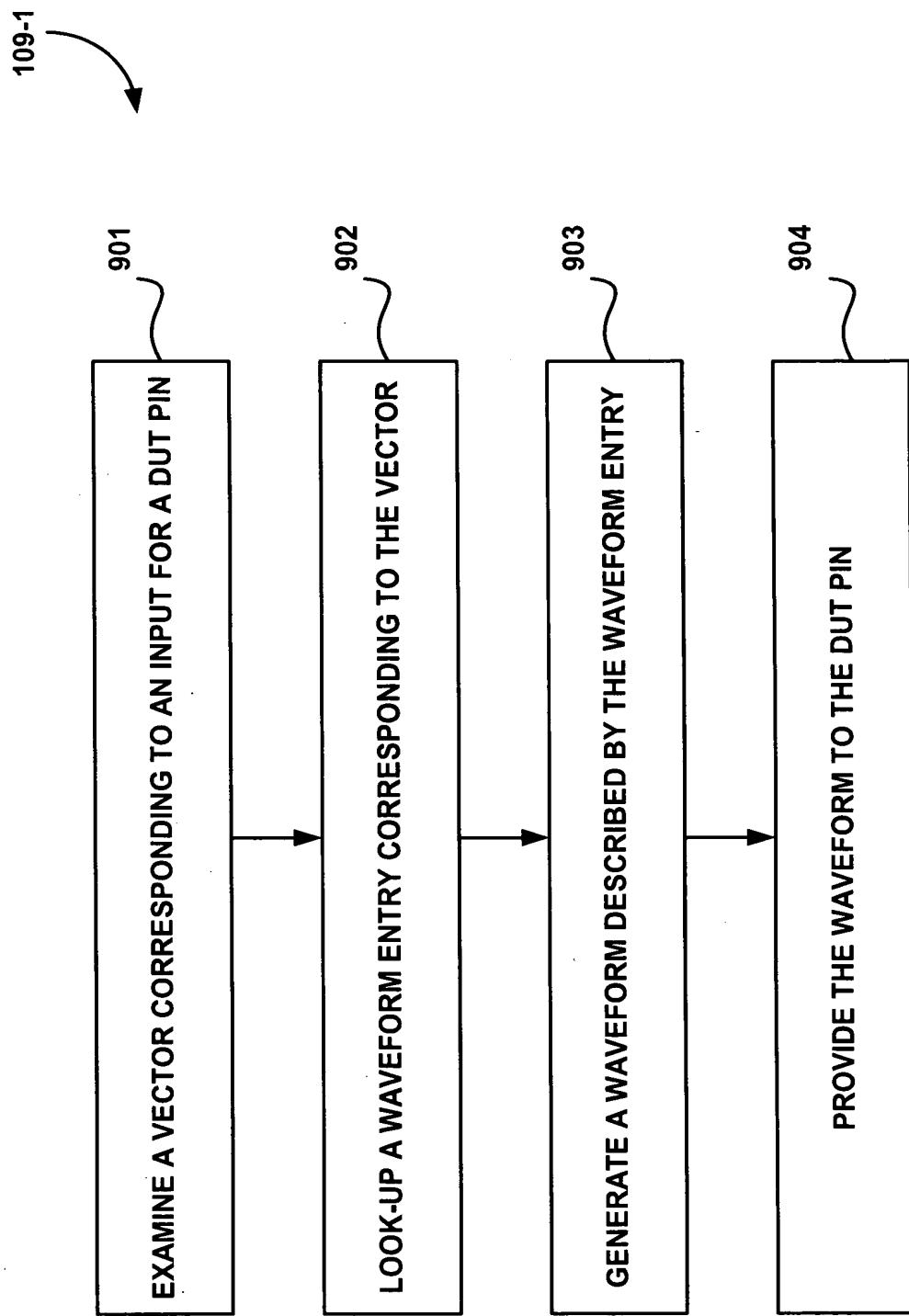


FIG. 9

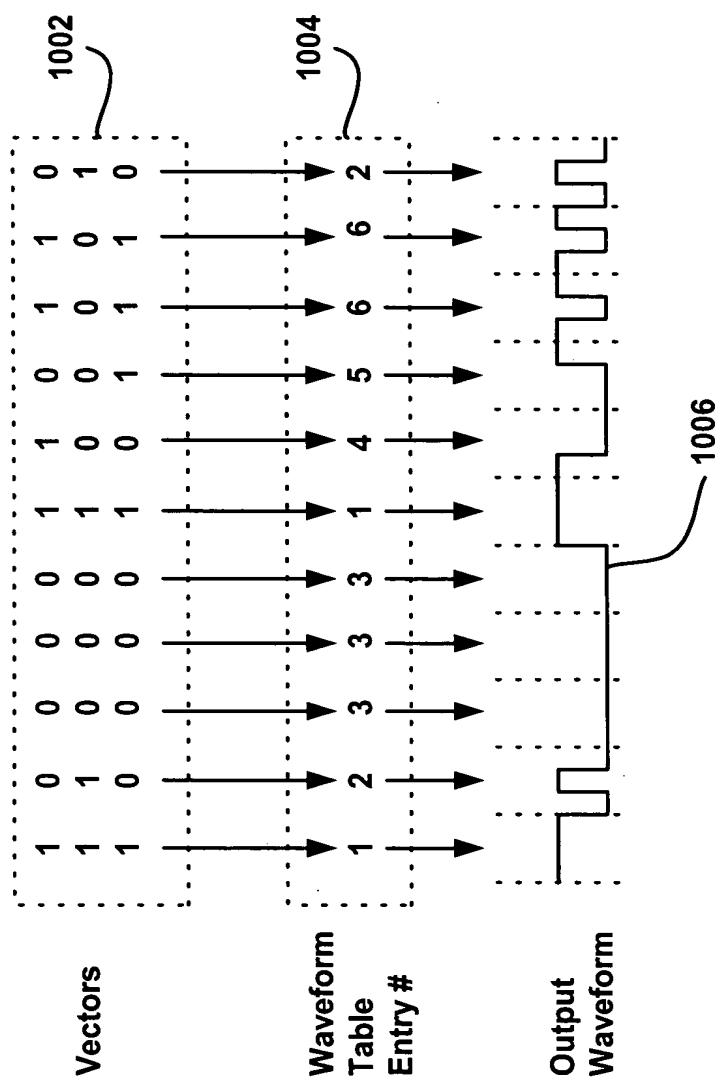


FIG. 10

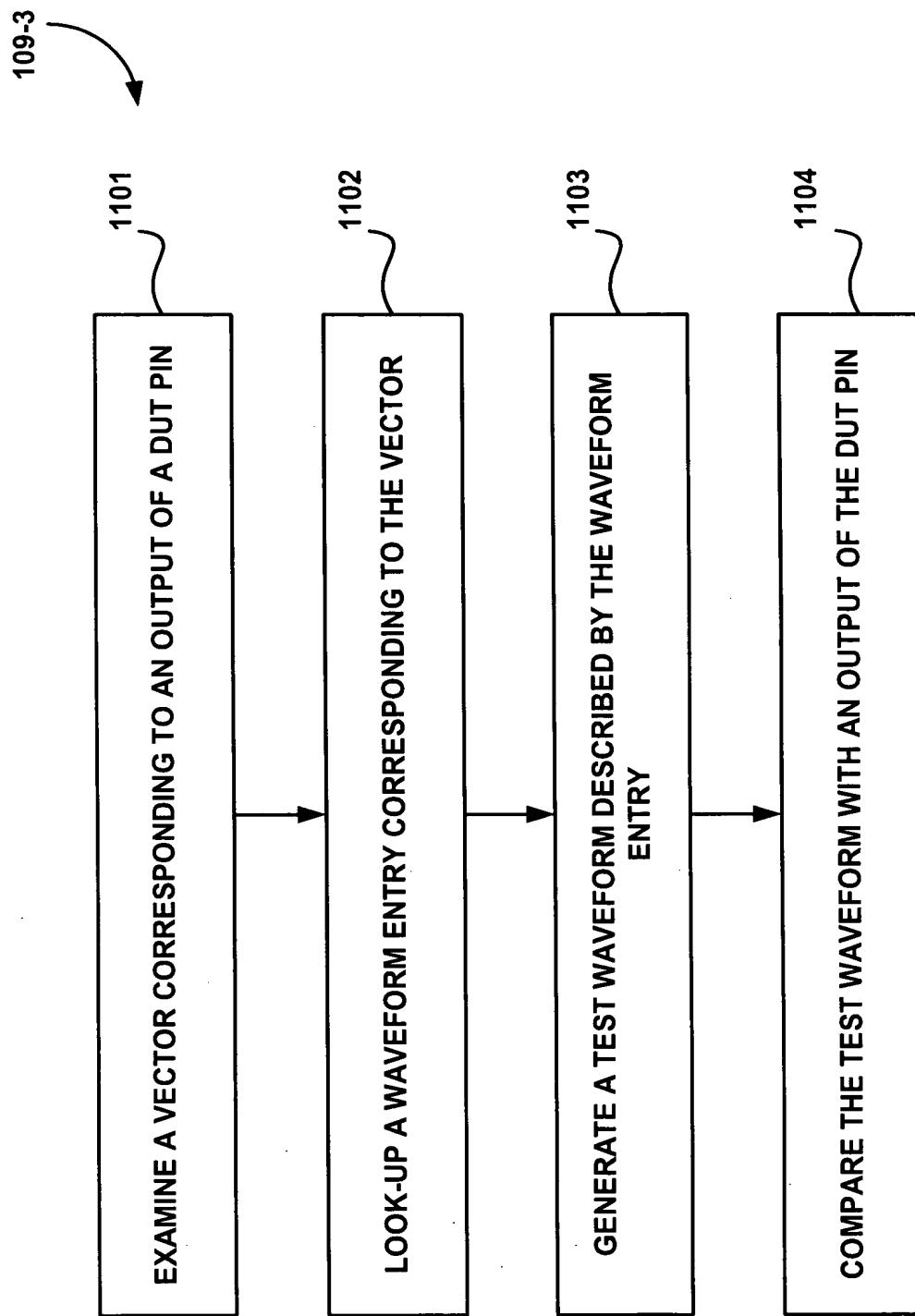


FIG. 11

FIG. 12

109

VECTOR MEMORY 1202

TESTING PROCESSOR 1200

DRIVE DATA FORMATTER 1210

EDGE GENERATOR 1208

COMPARE DATA FORMATTER 1216

SEQUENCER 1206

WAVEFORM TABLE 108

DUT PIN INTERFACE 1204

TO/FROM DUT PIN

COMPARATOR 1214

ERROR DETECTOR 1218

ERROR MAP 1220

